

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	194616	memory same (design\$3 or model\$3 or simulat\$3 or CAD or spice or verilog or vhdl or hdl)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:06
L8	28748	layout and L7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:06
L9	3939	ram and dram and L8	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:08
L10	19	sdr and ddr\$5 and L9	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:07
L11	4	module and L10	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:03
L12	4	(depend\$4 or non-depend\$4 or ("not" adj depend\$4)) and L11	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:03
L14	2290	"716"/8-11.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:01
L15	990	"716"/8.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:18
L16	476	"716"/9.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:36
L17	981	"716"/10.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:45
L18	823	"716"/11.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:59

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L19	66	sdr and ddr\$5 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:03
L20	14	module and 19	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:03
L21	11	(depend\$4 or non-depend\$4 or ("not" adj depend\$4)) and 20	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:15
L22	4	((computer near2 aid\$3) or CAD) and 21	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:05
L23	4386	(memory same (design\$3 or model\$3 or simulat\$3 or CAD or spice or verilog or vhdl or hdl)).clm.	US-PGPUB	OR	ON	2006/04/25 15:06
L24	831	layout and 23	US-PGPUB	OR	ON	2006/04/25 15:08
L27	158	layout.clm. and 23	US-PGPUB	OR	ON	2006/04/25 15:08
L28	3	sdr\$5.clm. and ddr\$5.clm. and 24	US-PGPUB	OR	ON	2006/04/25 15:10
L29	1	ram.clm. and dram.clm. and 27	US-PGPUB	OR	ON	2006/04/25 15:12
L31	6	(ram.clm. or dram.clm.) and 27	US-PGPUB	OR	ON	2006/04/25 15:13
L32	1	(depend\$4 or non-depend\$4 or ("not" adj depend\$4)).clm. and 31	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 15:17

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S69	2	"20040128628"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/11 08:20
S70	193504	memory same (design\$3 or model\$3 or simulat\$3 or CAD or spice or verilog or vhdl or hdl)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/11 09:35
S71	28574	layout and S70	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/11 09:37
S72	3905	ram and dram and S71	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/11 09:37
S73	19	sdr and ddr\$5 and S72	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/11 09:56
S74	4	module and S73	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/25 14:44